




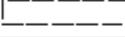












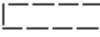
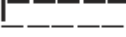









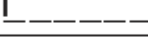








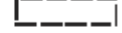







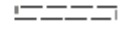




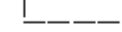
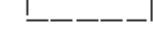
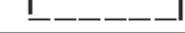






Drawing	Modules
	Q1
	Q2
	R1
	R2
	R3
	R4
	L1
	A1
	A2
	D1
	M1
	M2
	M3
	M4

Drawing	Modules
	Q1
	Q2
	R1
	R2
	R3
	R4
	L1
	A1
	A2
	D1

Drawing	Modules
	Q2
	R1
	R2
	R3
	R4
	R5
	L1
	A1
	A2
	D1

Drawing	Modules
	Q1
	Q2
	R1
	R2
	R3
	R4
	L1
	A1
	A2
	D1
	S1
	S2
	S3
	S4

Drawing	Modules
	Q2
	R1
	R2
	R3
	R4
	R5
	L1
	A1
	A2
	D1